

Electronic Acknowledgement Receipt

EFS ID:	9156338
Application Number:	09313659
International Application Number:	
Confirmation Number:	4199
Title of Invention:	METHOD OF FABRICATING A MOS TRANSISTOR WITH DOUBLE SIDEWALL SPACERS IN A PERIPHERAL REGION AND SINGLE SIDEWALL SPACERS IN A CELL REGION
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Filer Authorized By:	
Attorney Docket Number:	SEC.636
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Issue Fee Payment (PTO-85B)	Issue_Fee.pdf	111286	no	1
			c19c160245790176aaf23bac31bc1d02ade c35c		

Warnings:**Information:**

2	Fee Worksheet (PTO-875)	fee-info.pdf	30253	no	2
			c53a21f3608048b37c64f40c056443981eac0f a02		

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Total Files Size (in bytes):	141539
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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New International Application Filed with the USPTO as a Receiving Office

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